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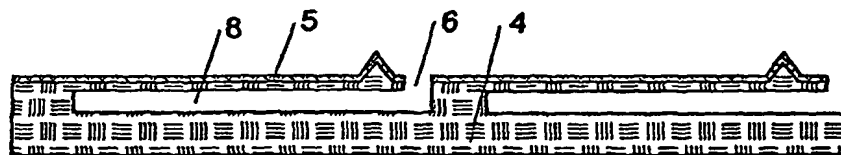
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etching pyramidal apertures in the first layer of doped silicon, removing the masking layer, depositing a second layer of doped silicon onto the first layer of doped silicon, the second layer of doped silicon being oppositely doped to the first layer of doped silicon and etching away the first layer of doped silicon. Further steps may be added to form the atomic force microscope tips at the end of cantilevers.

(57) Abstract: The invention relates to a method for forming silicon atomic force microscope tips. The method includes the steps of depositing a masking layer onto a first layer of doped silicon so that some square or rectangular areas of the first layer of doped silicon are not covered by the masking layer,

METHOD OF FORMING ATOMIC FORCE MICROSCOPE TIPS**FIELD OF INVENTION**

- 5 The invention relates to atomic force microscope tips and more particularly for a method of forming silicon atomic force microscope tips using etching techniques.

BACKGROUND

- 10 Atomic force microscope tips are used to measure the topography of surfaces at an atomic level. In atomic force microscopy an atomic tip is scanned over a surface and the interactions between the few atoms at the end of the tip and the surface are recorded. The sharpness of the tip, or its radius of curvature, determines the resolution of the scan. Some atomic force microscope tips are formed at the edge of a cantilever. With cantilevered
15 atomic force microscope tips the motion of the cantilever may be recorded to determine the topography of the surface being scanned. Atomic force microscopy may also be used in the manufacture of integrated circuits (IC) to measure the dimensions of components on the IC. There are also developments of applications for atomic force microscopy in the area of data storage. In this application sharper tips will enable a higher data density to be
20 achieved since more data bits can be written per square area.

- One method for forming atomic force microscope tips uses conventional electron beam chemical vapour deposition (CVD). This method as described in US patent 5,611,942 involves first forming a rounded tip. Masks are then added to the tip in areas where points
25 will be formed. The mask covered tip is then etched for a predetermined period of time. The etching process removes the tip material in a parabolic manner between the masks leaving points under the mask layers. Finally the mask layer is removed to reveal a multi-pointed atomic force microscope tip. One problem with this method is ensuring uniformity of the etching process to form uniform tips. A further problem is that the tips tend to break
30 off before they are sharp, leaving a fractured end.

Another method for forming atomic force microscope tips is to etch pyramidal pits into silicon with an etchant such as potassium hydroxide (KOH). These pits are then used as a mould for a silicon nitride tip. Common materials deposited in the mould include polymers, silicon dioxide and silicon nitride. Any moulded tip formed in this manner must be separated from the mould. In addition to the difficulty in ensuring a clean separation of the tips from the moulds, this method may not be applicable to more complex types of MEMS (microelectromechanical) structures. Further to this tips formed from silicon have superior physical properties, such as high strength and stiffness, when compared to tips formed from other materials.

SUMMARY OF INVENTION

It is the object of the invention to provide a method for forming atomic force microscope tips in silicon that overcome the disadvantages mentioned above or to at least provide the public with a useful choice.

In broad terms the invention comprises a method for forming a silicon atomic force microscope tip including the steps of depositing a masking layer onto a first layer of doped silicon so that some square or rectangular areas of the first layer of doped silicon are not covered by the masking layer, etching pyramidal apertures in the first layer of doped silicon, removing the masking layer, depositing a second layer of doped silicon onto the first layer of doped silicon, the second layer of doped silicon being oppositely doped to the first layer of doped silicon and etching away the first layer of doped silicon.

Before the first layer of doped silicon is etched the invention may further include the steps of performing an anisotropic wet etch on the second layer of silicon to provide at least one raised area, fusion bonding a third layer of silicon over the raised area to form at least one cavity between the second layer and the third layer. Following the step of etching away the first layer of doped silicon the invention may further include the steps of depositing a masking layer over the second layer of silicon on the side previously in contact with the first layer of silicon, patterning the masking layer to include an area of no masking at one

side of a tip, performing a release etch to remove silicon above the cavity not covered by the masking layer, and removing the masking layer.

BRIEF DESCRIPTION OF DRAWINGS

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A preferred from method of the invention will be further described with reference to the accompanying drawings by way of example only and without intending to be limiting, wherein;

10 Figure 1A shows a layer of silicon partially covered with a masking layer;

Figure 1B shows the layer of silicon and masking layer after a wet etch;

Figure 1C shows a second layer of silicon bonded to the first layer of silicon;

15

Figure 1D shows the results of an anisotropic etch on the second layer of silicon;

Figure 1E shows a third layer of silicon bonded to the second layer of silicon;

20 Figure 1F shows a masking layer deposited onto the second layer of silicon, and

Figure 1G shows a cantilevered atomic force microscope tip formed by a release etch on the second layer of silicon.

25 DETAILED DESCRIPTION

Figure 1A shows a layer of silicon 1 substantially covered by a masking layer 2. The masking layer is deposited such that rectangular or square areas 7 are uncovered. These areas are those where atomic force microscope tips will be formed. The silicon is either p-

30 p-type doped silicon or n-type doped silicon.

After depositing the masking layer 2 on the silicon pyramidal openings are formed in the doped silicon layer by etching with a strong base etchant, for example potassium hydroxide (KOH).

- 5 The crystal structure of silicon is a zinc blende lattice with identical atoms in each sublattice. The silicon crystal can be oriented along several planes. These planes are denoted by coordinates. For example $\langle 100 \rangle$, $\langle 110 \rangle$ and $\langle 111 \rangle$ are all different planes within the silicon crystal. When etching a single crystal, certain etchants exhibit orientation dependent etch rates. In particular strong base etchants, such as potassium
- 10 hydroxide (KOH), tetramethyl ammonium hydroxide (TMAH) and ethylene diamine pyrochatecol (EDP), exhibit highly orientation dependent etch characteristics in silicon.

Using a strong base etchant the etch rate in silicon is highly dependent on the direction of the crystal plane in the silicon. The $\langle 100 \rangle$ and $\langle 110 \rangle$ planes etch much faster than the

15 $\langle 111 \rangle$ plane. The aspect ratio of the tip is therefore limited by the crystallographic structure of the etch pit material. Using single crystal silicon the $\langle 111 \rangle$ planes effectively form etch stops that enable a mould with accurate dimensions and a sharp tip to be formed. The sides of the aperture formed in the single crystal silicon forms an etch angle of 54.7° to the horizontal. Varying the mask opening sizes can easily be used to form different tip

20 heights. The size of the apertures formed using strong base etching can therefore be controlled by the size of the aperture in the masking layer.

Figure 1B shows the results of the etching process. The etchant is ideally a strong base and may be, for example, KOH, TMAH or EDP. In a KOH etch a 45% concentration is

25 commonly used as an etchant. Isopropyl alcohol is also sometimes added to improve the selectivity of the etch. The common temperature range of a KOH etch is between 50°C and 85°C . The temperature of the etch affects the etch rate with the etch rate increasing as the temperature increases.

30 Following etching with the strong base etchant the masking layer is removed using known techniques. A layer of epitaxial silicon is then deposited onto the layer of doped silicon

and fills the apertures formed by the KOH etching as shown in Figure 1C. The layer of epitaxial silicon is doped to have the opposite doping of the first layer of silicon. For example if the first layer of silicon is p-type doped then the layer of epitaxial silicon is n-type doped and if the first layer of silicon is n-type doped then the layer of epitaxial silicon is p-type doped.

Figure 1D shows the results of an anisotropic etch. This etch forms troughs and raised areas in the epitaxial silicon layer 4. In Figure 1E a second layer of silicon has been fusion bonded to the layer of epitaxial silicon. This forms cavities 8 between the two layers. Ideally the second layer of silicon has the same doping as the layer of epitaxial silicon.

Following the bonding of the two layers of silicon the first layer of silicon 1 is removed by electrochemical etch. Because the first layer of silicon and the epitaxial layer of silicon 4 are doped differently the boundary between the two layers forms a diode junction. This diode junctions acts as a stop for the electrochemical etch. This allows the first layer of silicon 1 to be removed leaving the tips formed in the layer of epitaxial silicon 4 and solves the problems of removing the tips from the mould.

Figure 1F shows the layer of epitaxial silicon 4 with cavity 8 after removal of the first layer of silicon. As masking layer 5 is deposited onto the epitaxial silicon 4. The masking layer 5 covers the atomic force microscope tips but is deposited to leave a gap after each tip.

Figure 1G shows the results of a release etch. This etches away the epitaxial silicon not covered by masking layer 5. This releases the tip from the nearest raised layer of epitaxial silicon 4 and leaves the tip cantilevered. Finally the masking layer 5 is removed to leave the cantilevered atomic force microscope tips.

Other process steps can be added to this process to form other MEMS structures such as actuators or features such as metal interconnects/bondpads.

In another embodiment the steps illustrated in Figures 1D to 1G are omitted and the first layer of silicon 1 is removed by electrochemical etch after the step of depositing the layer

of epitaxial silicon 4 shown in Figure 1C. In this embodiment the atomic force microscope tips formed using the method of the invention are not cantilevered. Again further steps can be added to this process to form other MEMS structures or other features.

- 5 Using the method of the invention atomic force microscope tips have been produced with a base of 4×4 microns and height of 2.82 microns. The tips produced using the method of the invention are formed of silicon and have superior stiffness and strength to tips formed of other materials such as silicon nitride, silicon dioxide etc. Atomic force microscope tips formed using the method of the invention are more uniform than those formed using
10 chemical vapour deposition.

The foregoing describes the invention including preferred forms thereof. Alterations and modifications as will be obvious to those skilled in the art are intended to be incorporated within the scope hereof as defined in the accompanying claims.

CLAIMS

1. A method of forming silicon atomic force microscope tips including the steps of:
depositing a masking layer onto a first layer of doped silicon so that some square or
5 rectangular areas of the first layer of doped silicon are not covered by the masking
layer,
etching pyramidal apertures in the first layer of doped silicon,
removing the masking layer,
depositing a second layer of doped silicon onto the first layer of doped silicon, the second
10 layer of doped silicon being oppositely doped to the first layer of doped silicon, and
etching away the first layer of doped silicon.
2. A method of forming silicon atomic force microscope tips as claimed in claim 1
wherein the final etch is an electrochemical etch.
15
3. A method of forming silicon atomic force microscope tips as claimed in claim 1 or
claim 2 wherein the first layer of silicon is p-type doped silicon and the second layer of
silicon is n-type doped silicon.
- 20 4. A method of forming silicon atomic force microscope tips as claimed in claim 1 or
claim 2 wherein the first layer of silicon is n-type doped silicon and the second layer of
silicon is p-type doped silicon.
5. A method of forming silicon atomic force microscope tips as claimed in any one of
25 claims 1 to 4 wherein the pyramidal apertures are etched using a strong base etchant.
6. A method of forming silicon atomic force microscope tips as claimed in claim 5
wherein the strong base etchant is potassium hydroxide.
- 30 7. A method of forming silicon atomic force microscope tips as claimed in claim 5
wherein the strong base etchant is tetramethyl ammonium hydroxide.

8. A method of forming silicon atomic force microscope tips as claimed in claim 5 wherein the strong base etchant is ethylene diamine pyrochatecol.

9. A method of forming silicon atomic force microscope tips as claimed in any one of
5 claims 1 to 8 further including the steps of:

before the first layer of doped silicon is etched performing an anisotropic wet etch on the
second layer of silicon to provide at least one raised area,

fusion bonding a third layer of silicon over the raised area to form at least one cavity
between the second layer and the third layer,

10 following the step of etching away the first layer of doped silicon depositing a masking
layer over the second layer of silicon on the side previously in contact with the first
layer of silicon,

patterning the masking layer to include an area of no masking at one side of a tip,

performing a release etch to remove silicon above the cavity not covered by the masking

15 layer, and

removing the masking layer.

10. A method of forming silicon atomic force microscope tips as claimed in claim 9 wherein the third layer of silicon has the same doping as the second layer of silicon.

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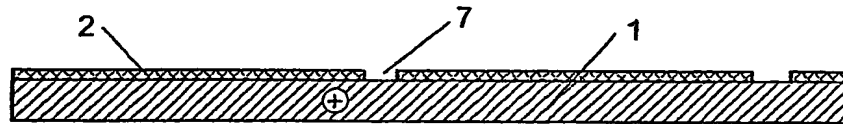


FIGURE 1A

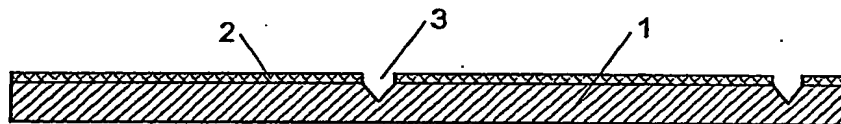


FIGURE 1B

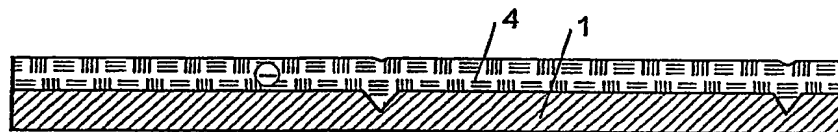


FIGURE 1C



FIGURE 1D

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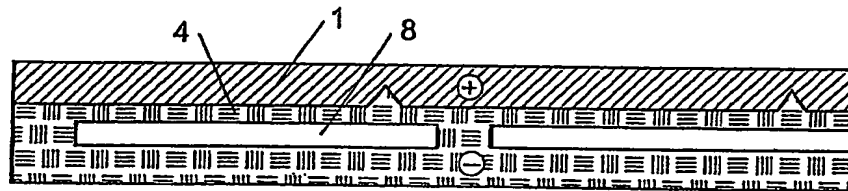


FIGURE 1E

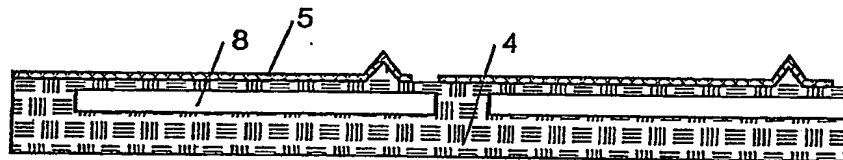


FIGURE 1F

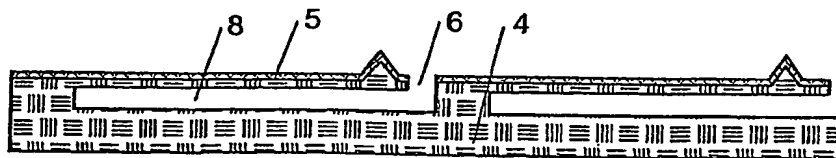


FIGURE 1G